			IV.
	Application No.	Applicant(s)	
Notice of Allowability	10/091,194	NADD ET AL.	
	Examiner	Art Unit	
	Alonzo Chambliss	2827	
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject t	plication. If not included n will be mailed in due co	ourse. THIS
⊠ This communication is responsive to amendment A filed or ⊠ The allowed claim(s) is/are 1.18. □ The drawings filed on are accepted by the Examine □ All Acknowledgment is made of a claim for foreign priority ur a) All b) □ Some* c) □ None of the: 1. □ Certified copies of the priority documents have 2. □ Certified copies of the priority documents have	r. nder 35 U.S.C. § 119(a)-(d) or (f). been received. been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the			
International Bureau (PCT Rule 17.2(a)).			
Certified copies not received:	ation or in an Application Data Shee application has been received. ander 35 U.S.C. §§ 120 and/or 121 s	t. 37 CFR 1.78.	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of below. Failure to timely comply will result in ABANDONMENT of	this communication to file a reply c	omplying with the require	ments noted
7. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give			TICE OF
8. CORRECTED DRAWINGS (as "replacement sheets") mus (a) Including changes required by the Notice of Draftspers 1) hereto ro 2) Including the No. 4. (b) including changes required by the proposed drawing of (c) including changes required by the attached Examiner's	on's Patent Drawing Review (PTO-	een approved by the Exa	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	.84(c)) should be written on the drawi he margin according to 37 CFR 1.121(ngs in the front (not the bad).	ack) of
 DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT FOR T 	sit of BIOLOGICAL MATERIAL 1 HE DEPOSIT OF BIOLOGICAL MA	must be submitted. No TERIAL.	te the
Attachment(s)			
1⊠ Notice of References Cited (PTO-892)	5 Notice of Informal Pa	atent Application (PTO-15	52)
 2 Notice of Draftperson's Patent Drawing Review (PTO-948) 3 Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No. 	6☐ Interview Summary (PTO-413), Paper No	·
	i). 7⊠ Examiner's Amendm	ent/Comment	
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8⊠ Examiner's Statemer 9∏ Other	nt of Reasons for Allowar	nce
		Alonzo Chambliss Patent Examiner Art Unit: 2827	

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DETAILED ACTION

 Amendment A filed on 8/8/03 has been fully considered and made of record in Paper No. 5.

EXAMINER'S AMENDMENT

- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 3. The application has been amended as follow:

IN SPECIFICATION:

The title has been changed to "H-BRIDGE DRIVE UTILIZING A PAIR OF HIGH AND LOW SIDE MOSFET'S IN A COMMON INSULATION HOUSING".

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee.

Allowable Subject Matter

4. The following is a statement of reasons for the indication of allowance subject matter: the prior art of record does not teach or suggest the combination of a H-bridge driver having first and second high side MOSFET's, wherein the first and second high side MOSFET's are mounted on first and second low side MOSFET's respectively. A

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control IC controlling the high side and low side MOSFET's, wherein the control IC has input terminals connected to receive input control signals from an exterior bridge control circuit and has output terminals connected to the control electrodes of the high side MOSFET's. A support for supporting the high side and low side MOSFET's. A common insulation housing for enclosing the first and second high side MOSFET's and the IC, wherein the first and second high side MOSFET has power switches having output terminals. The output terminals of the power switch communicating an output signal for the control IC shutdown the H-bridge when the switch current or MOSFET die temperature exceeds a threshold value in claim 1.

A H-bridge driver having first and second high side MOSFET's, wherein the first and second high side MOSFET's are mounted on first and second low side MOSFET's respectively. A control IC controlling the high side and low side MOSFET's, wherein the control IC has input terminals connected to receive input control signals from an exterior bridge control circuit and has output terminals connected to the control electrodes of the high side MOSFET's. A support for supporting the high side and low side MOSFET's. A common insulation housing for enclosing the first and second high side MOSFET's and the IC. Connection pins extending from the package, wherein the connection pins include IN1 and IN2 pins coupled to the control electrodes of the first and second high MOSFET's. A Vcc pin and a GND pin connected to the power input terminals with a M1 and M2 pin connected to the first and second nodes at the output bridge terminals in claim 2.

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A H-bridge driver having first and second high side MOSFET's, wherein the first and second high side MOSFET's are mounted on first and second low side MOSFET's respectively. A control IC controlling the high side and low side MOSFET's, wherein the control IC has input terminals connected to receive input control signals from an exterior bridge control circuit and has output terminals connected to the control electrodes of the high side MOSFET's. A support for supporting the high side and low side MOSFET's. A common insulation housing for enclosing the first and second high side MOSFET's and the IC. The low side MOSFET's both being normally ON in claim 3.

A H-bridge driver having first and second high side MOSFET's, wherein the first and second high side MOSFET's are mounted on first and second low side MOSFET's respectively. A control IC controlling the high side and low side MOSFET's, wherein the control IC has input terminals connected to receive input control signals from an exterior bridge control circuit and has output terminals connected to the control electrodes of the high side MOSFET's. A support for supporting the high side and low side MOSFET's. A common insulation housing for enclosing the first and second high side MOSFET's and the IC. The control IC includes a PWM generator for applying a soft start PWM signal to the control electrodes of the low side MOSFET's in claim 5.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning the communication or earlier communications from the examiner should be directed to Alonzo Chambliss whose telephone number is (703) 306-9143. The fax phone number for the Group is (703) 308-772 or 7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the group receptionist whose telephone number is (703) 308-7956.

AC/November 15, 2003

Alonzo Chambliss Patent Examiner Art Unit 2827